WEST Search History

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DATE: Tuesday, August 03, 2004

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DB=USPT; $PLUR=YES$; $OP=ADJ$			
	L19	L15 and 114	4
- S	L18	L15 and 11	23
	L17	L15 same 11	2
	L15	parallel near3 delay circuit	281
	L14	edge near3 detect\$ near10 clock	2318
	L13	13 same latch	13
	L12	13 same flip\$1 flop	3
	L11	13 near10 flip\$1 flop	1
*	L10	5357196.pn. and delay\$	1
7.55.84	L9	13.ab.	5
	L7	L6 near20 delay\$	4
, i	L6	L3 near5 edge	89
	L5	5357196[uref]	8
	L4	11 same L3	4
	L3	detect\$ near5 (rising near10 trailing)	99
de andre	L2	detect\$ near3 (rising near10 trailing)	77
- Constant	L1	pulse near3 generat\$ near3 clock	22384

END OF SEARCH HISTORY

Best Available Copy